

Figure 1 (Prior Art)

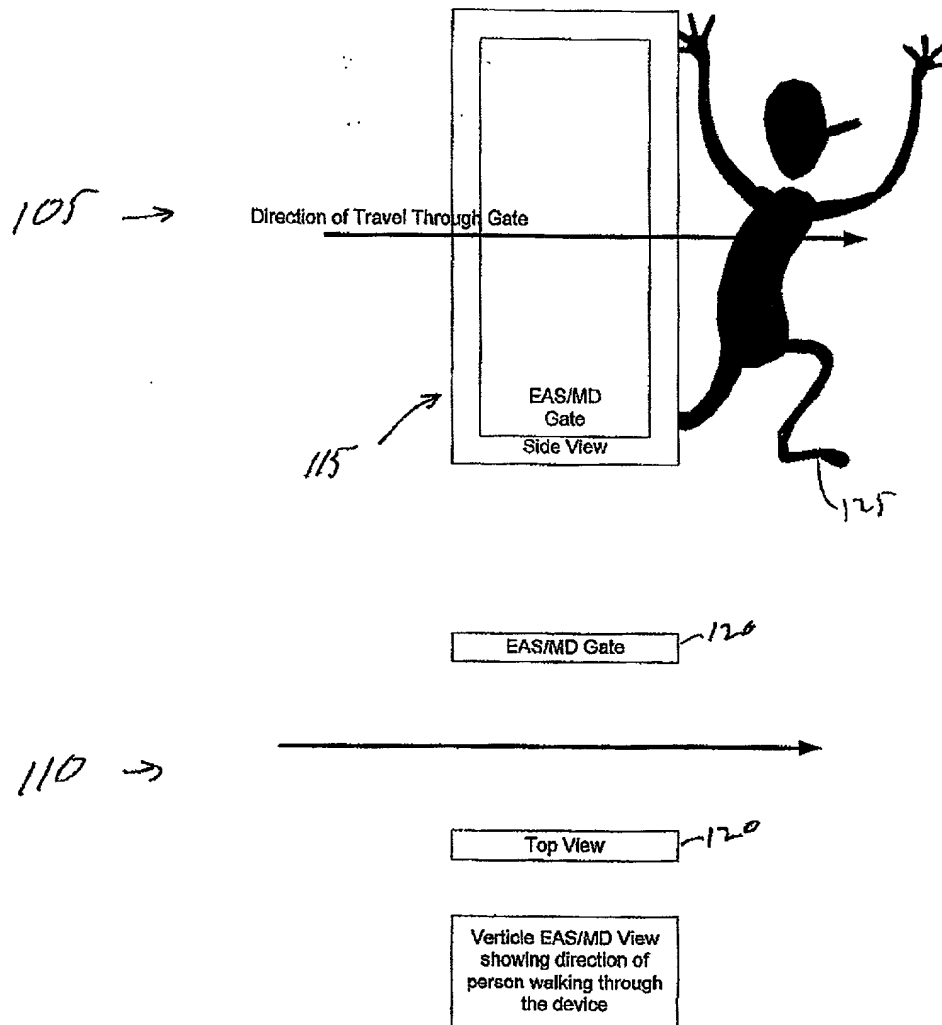


Figure 2 (Prior Art)

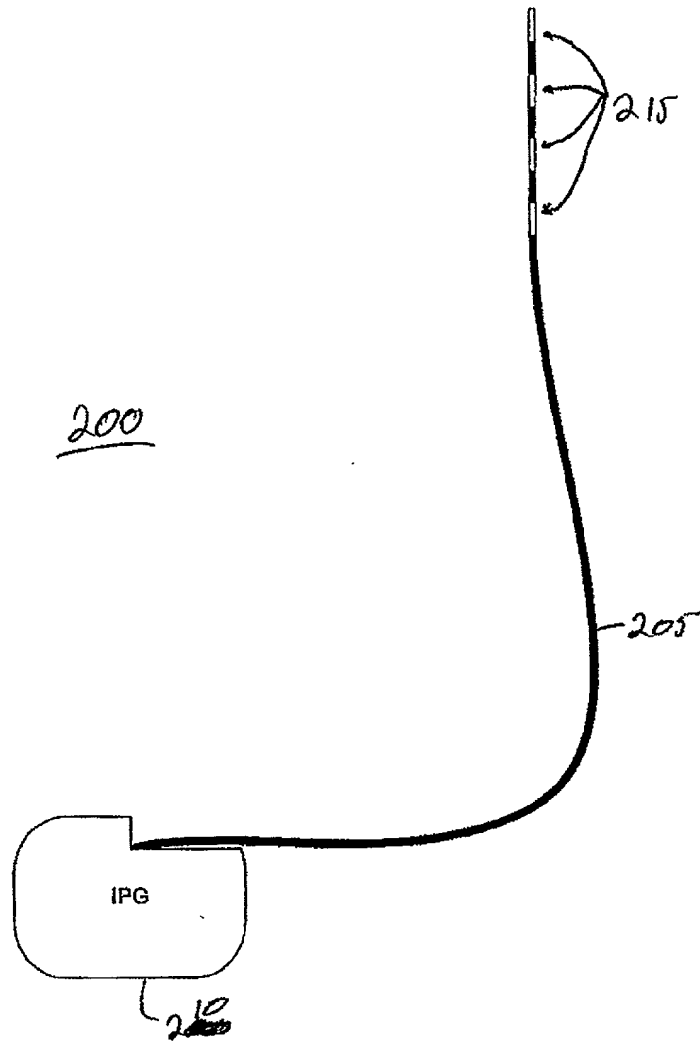


Figure 3 (Prior Art)

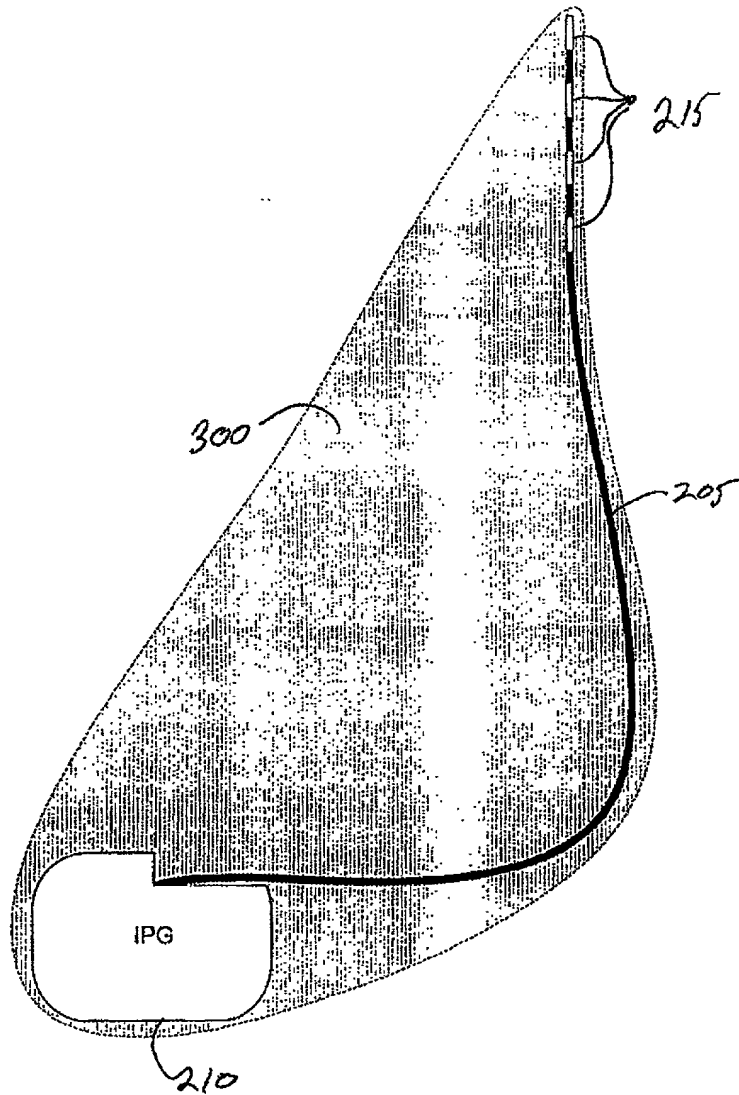


Figure 4 (Prior Art)

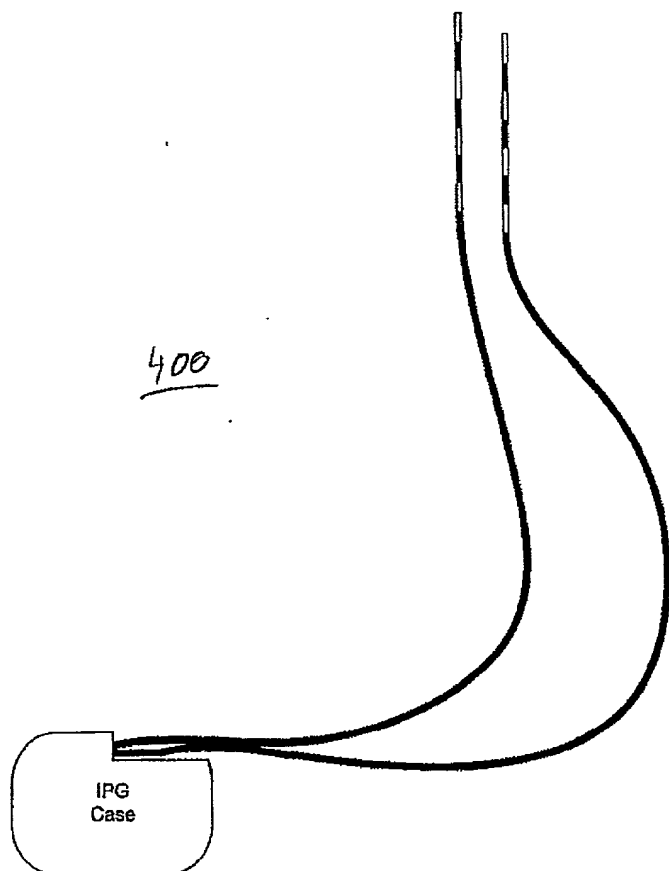


FIG. 4 is a schematic diagram of a prior art device.

Figure 5 (Prior Art)

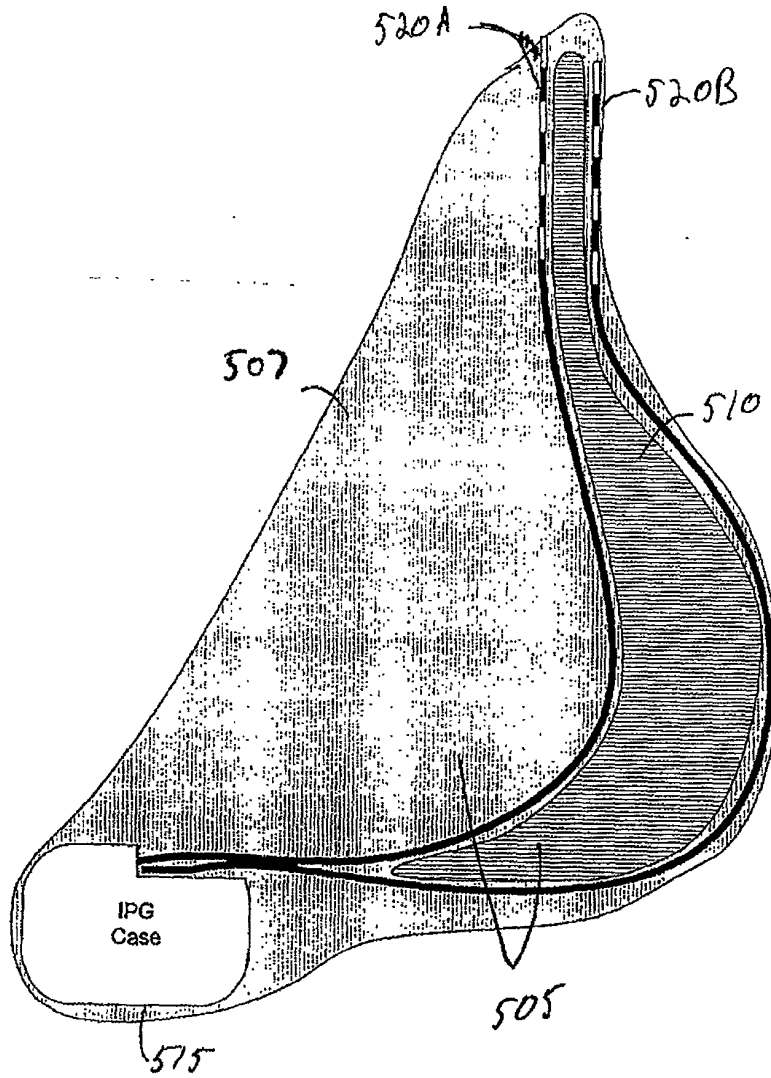
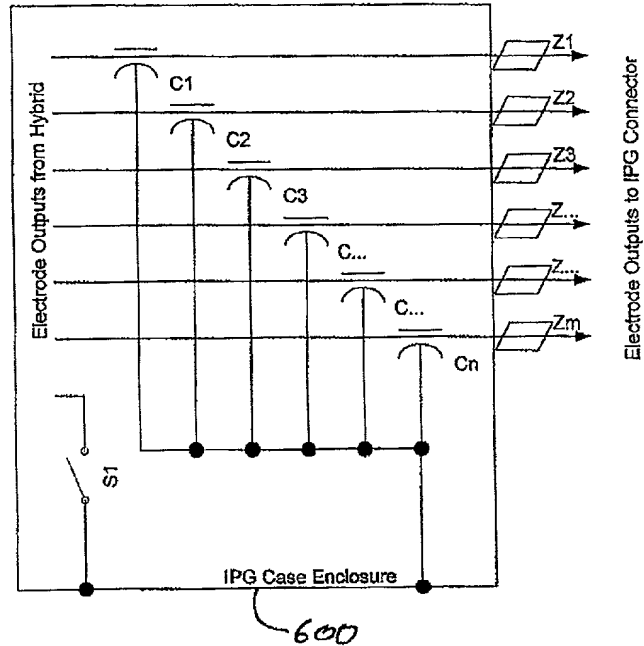


Figure 6



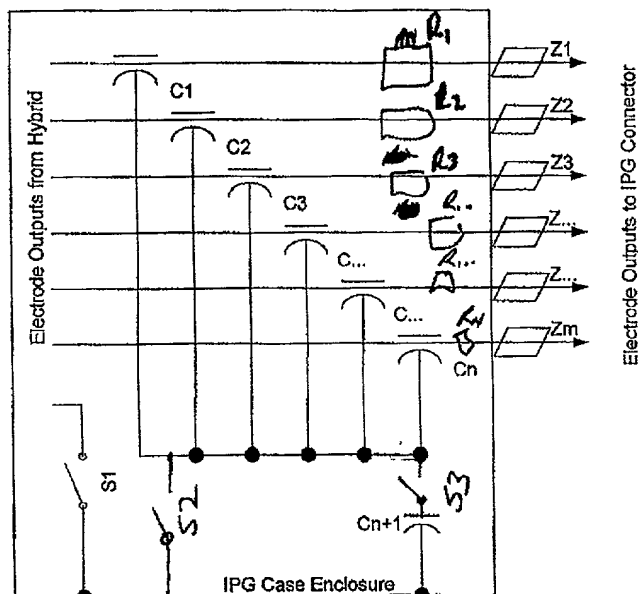
C1 thru Cn =
 Feedthrough Capacitors
 (part of feedthrough or separate capacitors)

Cn+1 = common EMC capacitor to IPG case

Z1 thru Zm =
 Impedance elements on outboard side of feedthroughs (may be ferrite
 bead, resistor, or inductor)

S1 = case electrode switch (may be electronic or mechanical such as
 a reed switch)

Figure 7



C1 thru Cn =
 Feedthrough Capacitors
 (part of feedthrough or separate capacitors)

Cn+1 = single case electrode

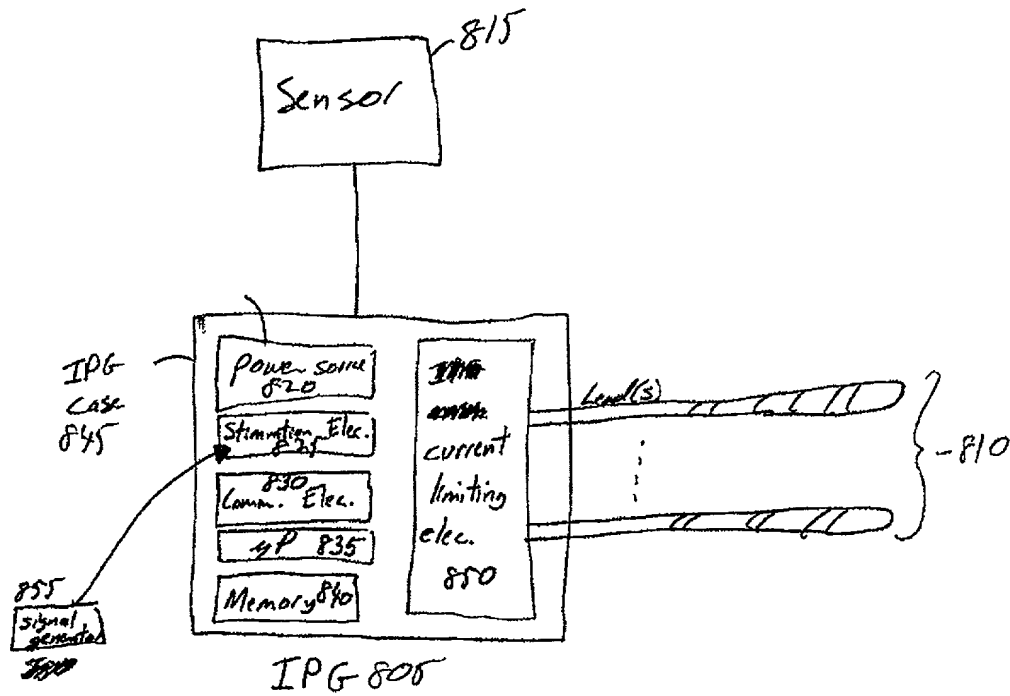
Z1 thru Zm = AC current blocking elements.
 Impedance elements on outboard side of feedthroughs capacitors
 (may be ferrite bead, resistor, or inductor)

S1 = optional switching device.
 S2 = optional switching device

S3 = optional switching device.

R1 - Rn = optional resistors

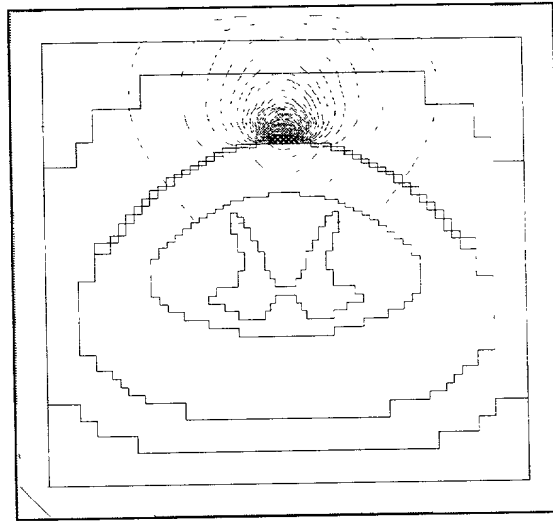
Figure 8



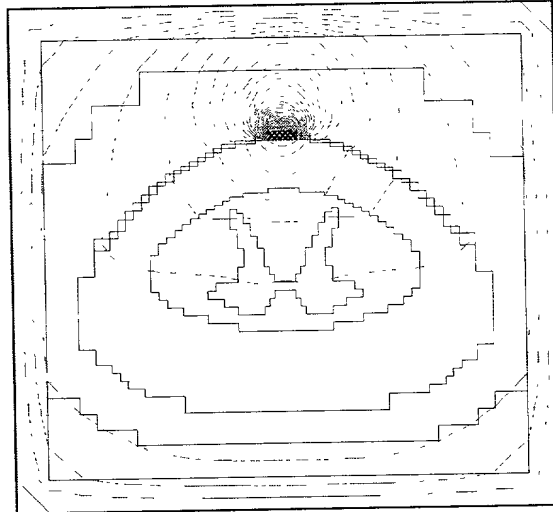
800

Figure 9: Potential fields - transverse view

Regular bipole (6.5 mm spacing)



Virtual monopole



Monopole

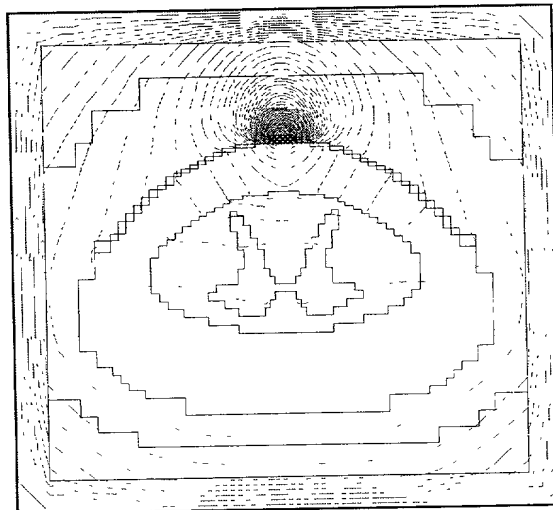
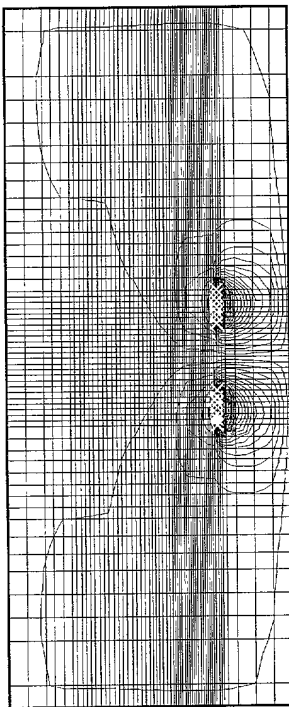
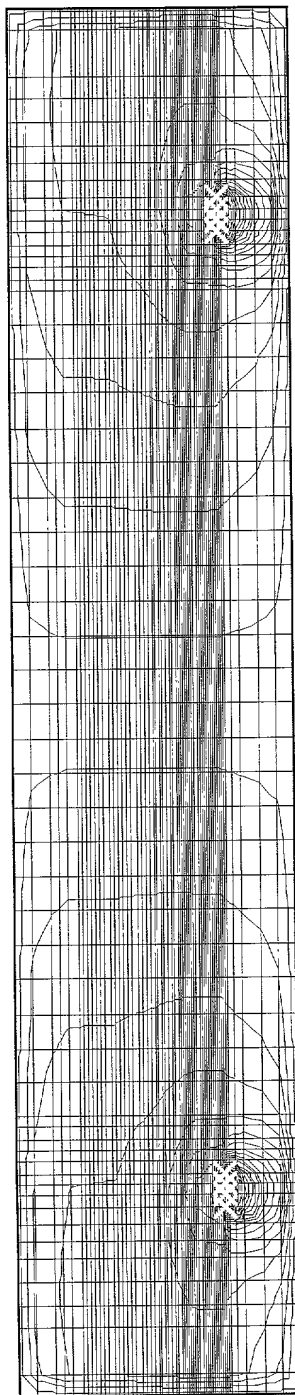


Figure 10: Potential fields - sagittal view

Regular bipole



Virtual monopole



Monopole

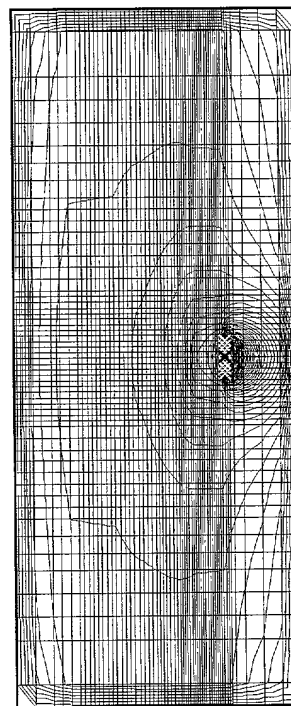
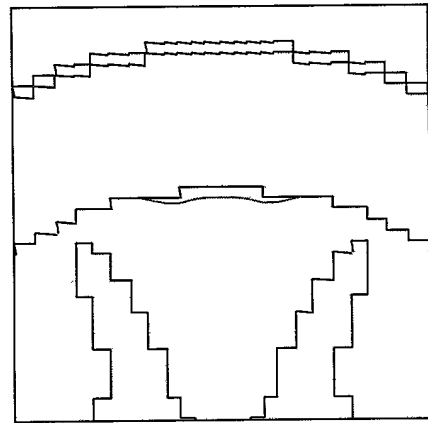
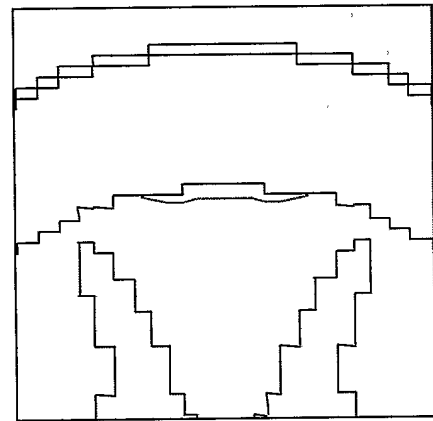


Figure 11: Recruitment zones

Regular bipole (6.5 mm spacing)



Virtual monopole



Monopole

